

## **ABSTRACT OF THE DISCLOSURE**

An interleaving management for upgrading the operation speed of flash memory cell is provided. A mother and child concept is applied to process the data of a flash memory cell. The so-called mother and child are two physical features own one logical address at the same time to allow the host to write to a logical address. Thus the controller need not repeat the transferring and erasing actions while writing data to the flash memory cell, accordingly the life of the flash memory cell can be extended and the processing speed can be substantially upgraded.